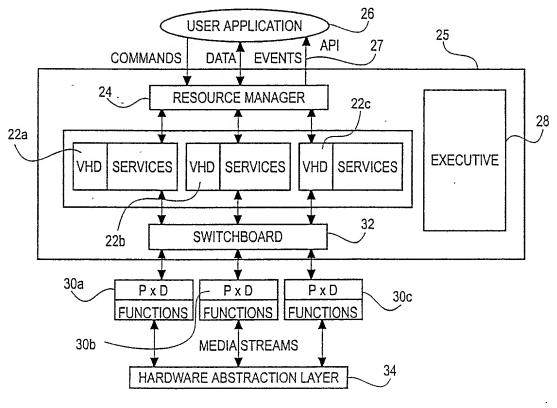
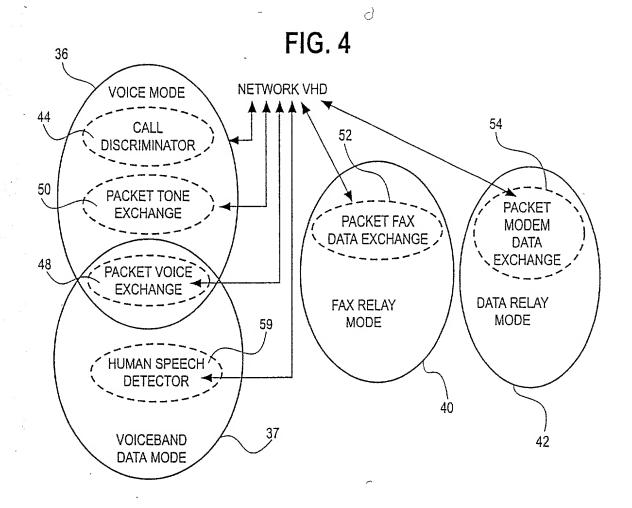


FIG. 3



TO UNDERLYING HARDWARE



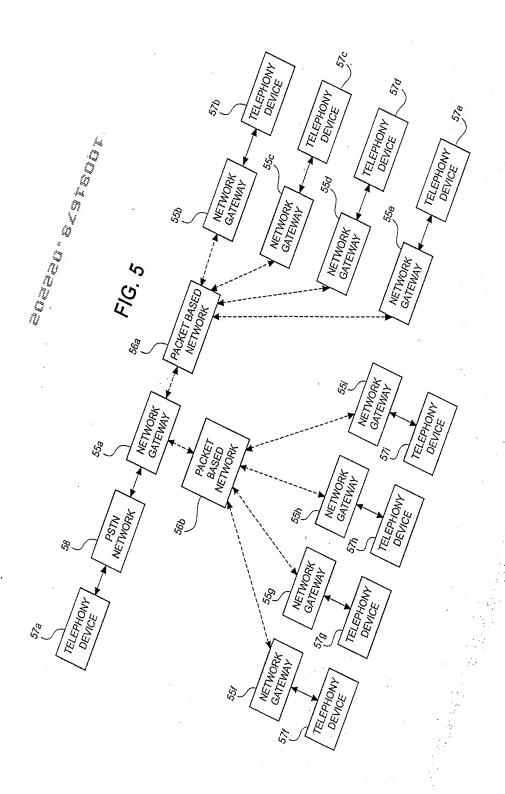
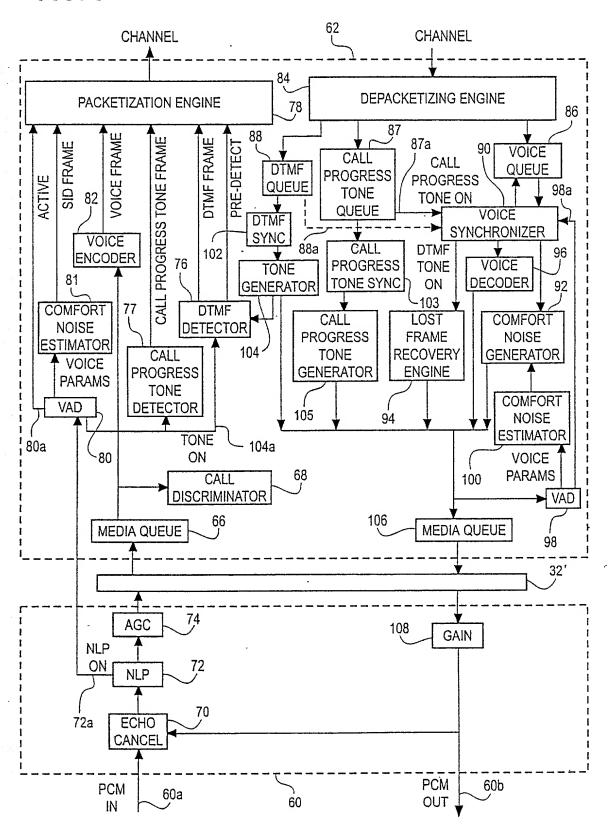


FIG. 6



To Packetization Engine

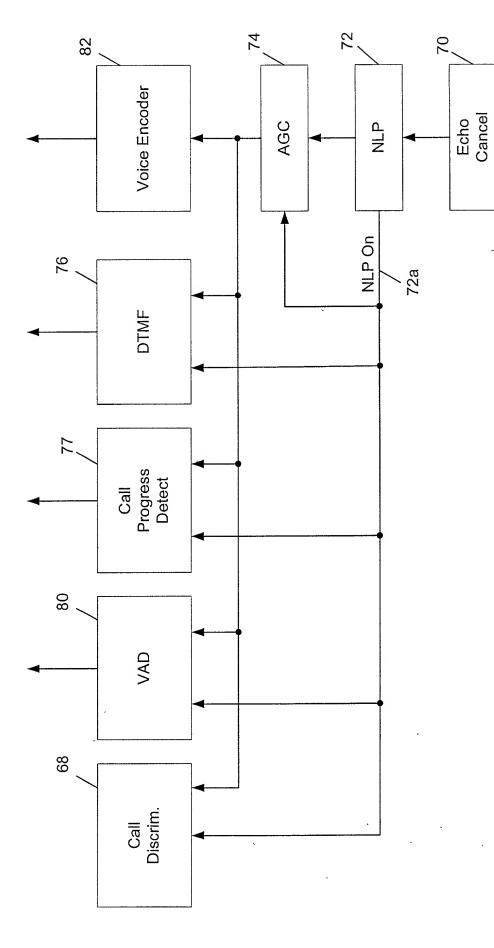


FIG. 7

60a

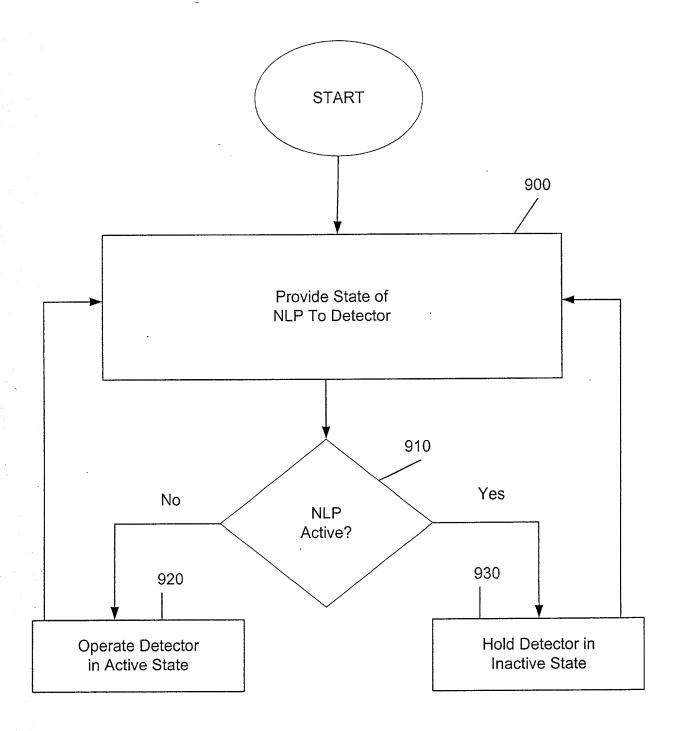


FIG. 8

To Packetization Engine

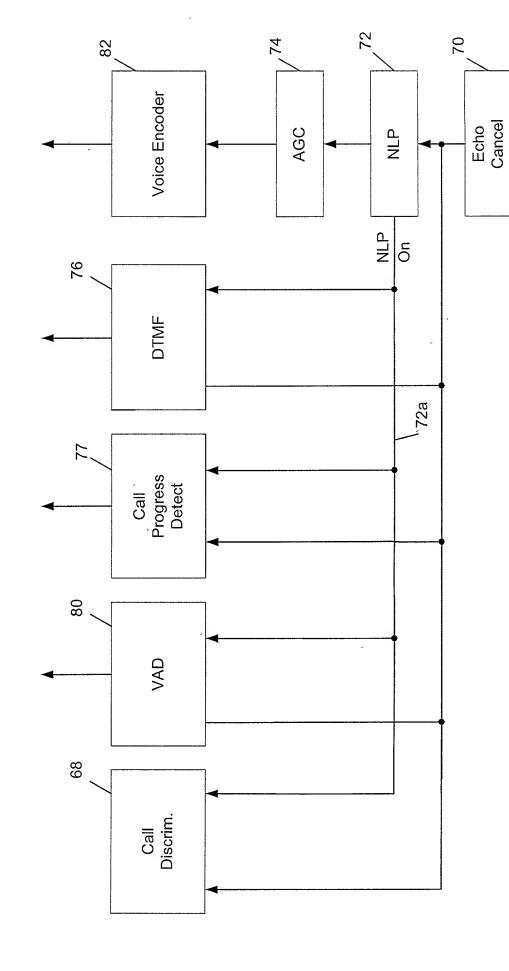


FIG. 9

√ 60a